



PATENT
Docket No. INTEL/16136X2

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicant(s): Mingqiu SUN

Serial No.: 10/608,324

Filed: June 27, 2003

For: "Methods and Apparatus to Prefetch
Memory Objects"

Group Art Unit: 2183

Examiner: Not Yet Assigned

I hereby certify that this paper is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this date:

December 14, 2005

Sarah Doyle
Sarah Doyle

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The documents listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the non-U.S. patent documents are enclosed.

TIME OF FILING

This information disclosure statement is being filed to the best of the undersigned's knowledge, before the mailing date of a first Office action. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

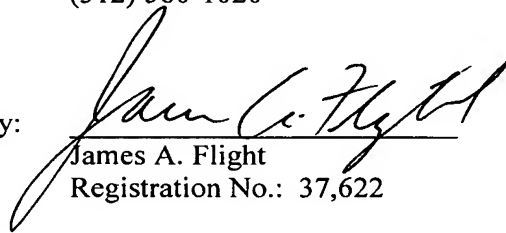
The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

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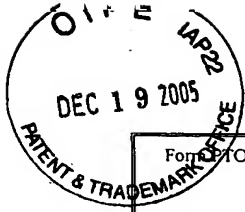
By:



James A. Flight
Registration No.: 37,622

December 14, 2005

Attorneys for Intel Corporation



Form PTO-1449 (Modified)

U.S. Department of Commerce
Patent and Trademark Office

Atty. Docket No.

20002/16136X2

Serial No.

10/608,324

Applicant

Mingqiu SUN

Filing Date

June 27, 2003

Group Art Unit

2183

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	INVENTOR(S)	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01	2002/0152361	10-2002	Dean et al.			
	A02	6,571,318	05-2003	Sander et al.			
	A03	6,237,065	05-2001	Banerjia et al.			
	A04	5,850,632	12-1998	Robertson, Iain			
	A05	6,622,168	09-2003	Datta, Anindya			
	A06	5,606,685	02-1997	Frandeen, James W.			
	A07	6,415,359	07-2002	Kimura et al.			
	A08	6,752,335	06-2004	Levia, Kimothy			
	A09	6,751,707	06-2004	Magoshi, Hidetaka			
	A10	2001/0042172	11-2001	Duesterwald et al.			
	A11						
	A12						
	A13						
	A14						
	A15						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	C01	Hazelwood et al., <i>Code Cache Management Schemes for Dynamic Optimizers</i> , Proceedings of the Sixth Annual Workshop on Interaction between Compilers and Computer Architectures (INTERACT'02), 2002, 9 pages.
	C02	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.